MoVeP 2012

10th International Winter School on MOdelling and VErifying parallel Processes

December 3–7 2012, Marseille, France

http://movep.lif.univ-mrs.fr/

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Important Dates:

September 3, 2012 Opening of registration October 19, 2012 Submission deadline November 2, 2012 End of registration December 3–7, 2012 Winter school

Registration Fees:

Double room 410 € Single room 450 €

The registration fee include the full accommodation, attendance to the scientific program, course materials, coffee breaks and a conference dinner. A limited number of grants for students will be available.

Further Information:

Local Organisation: Pierre-Alain Reynier E-mail: movep2012@lif.univ-mrs.fr

About MoVEP

MoVEP is a 5 day school about modelling and verifying parallel processes. The first five MoVEP schools took place in Nantes (France) every two years from 1994 to 2002. The next editions were held in Brussels (Belgium) in December 2004, in Bordeaux (France) in June 2006, in Orleans (France) in June 2008, and in Aachen (Germany) in June 2010.

The purpose of MoVEP is to bring together researchers, students and people from industry working in the fields of control and verification of concurrent and reactive systems. The school seeks to offer a broad spectrum of current research in this area of theoretical and applied computer science. The topics covered by MoVEP 2012 include model checking, synthesis, abstract interpretation, real-time and probabilistic systems, logic, games, Petri nets, security, etc.

MoVEP 2012 is a thematic school of the CNRS. It is also supported by the CIRM, the INRIA, Action Afsec of the CNRS GDR ASR, AIX-MARSEILLE UNIVERSITY, the LIF, and the CG13.

Program

Tutorials (2h30):

- 1. Logic and Verification
 Moshe Y. Vardi (Rice University, US)
- 2. Probabilistic Systems
 Marta Kwiatkowska (Oxford University, GB)
- 3. Unfoldings: A Partial Order approach to Model Checking Javier Esparza (TU München, DE)
- 4. Timed automata and their quantitative extensions Kim G. Larsen (Aalborg University, DK)
- 5. Games for Verification and Synthesis Hugo Gimbert (LaBRI, Bordeaux, FR)
- 6. Analysis of Extended AADL Models
 A. Cimatti (IRST, IT) and T. Noll (RWTH Aachen, DE)

Technical Talks (1h30):

- 1. Runtime Verification
 Martin Leucker (TU München, DE)
- 2. Computer-Aided Cryptographic Proofs and Designs Gilles Barthe (IMDEA, Madrid, ES)
- 3. Static analysis by abstract Interpretation Antoine Miné (ENS Paris - CNRS, FR)
- 4. Verification of concurrent systems Ahmed Bouajjani (LIAFA, Paris, FR)
- 5. Software Synthesis Ruzica Piskac (MPI-SWS, DE)

Ph.D. student sessions

In addition to the tutorials and talks, there will be special sessions where Ph.D. students have the possibility to present their on-going research. Extended abstracts have to be submitted electronically until October 19th (see the web site for detailed submission instructions).